

Abstract

An integrated transceiver circuit has a reception path, which in turn has a mixer unit for demodulation of a received signal. An analog/digital converter unit is connected in the reception path downstream from the mixer unit. A first frequency divider is connected between the mixer unit and a voltage controlled
5 oscillator in order to predetermine the demodulation frequency. A second frequency divider is connected between the oscillator and the analog/digital converter unit in order to predetermine the sampling frequency.